

**Dr. N.Mohankumar** was born in India in 1978. He received his B.E. Degree from Bharathiyar University, Tamilnadu, India in 2000 and M.E. & Ph.D Degree from Jadavpur University, Kolkata in 2004 & 2010. He joined the Nano Device Simulation Laboratory in 2007 and worked as a Senior Research Fellow under CSIR direct Scheme till September 2009. Later he joined SKP Engineering College as a professor to develop research activities in the field of VLSI and NANO technology. He initiated and formed centre of excellence in VLSI and NANO technology at SKP Engineering college at the cost of 1 crore.

MoU was signed between SKP and Tokyo Institute of technology, Japan and New Jersey institute of technology under his guidance. In the year 2010 he visited Tokyo institute of technology, Japan as a visiting professor for the period of three months. In the year 2012 he visited New Jersey Institute of Technology, USA as a visiting professor for a period of 2 months.

He is a Senior Member of IEEE and served as Secretary of IEEE, EDS Calcutta chapter from 2007 to 2010. He is currently the founder Chairman of IEEE EDS Madras Chapter. He has about 51 International journal publications in reputed journals and about 50 international conference proceedings. He received the carrier award for young teachers (CAYT) from AICTE, New Delhi in the year of 2012-2014

He is a recognized supervisor for research under Anna University and Anna University of technology, Chennai. At present he is guiding 12 PhD Research scholars and 12 M.E students.

My research interest includes modeling and simulation study of nanoscale transistors, optimization of devices for RF applications and characterization of nanowire MOSFETs, III/V Compound semiconductors, their design and application as HEMTs and MOS-HEMTs