



**Aminul Islam** (M'10, SM'15) received the B.Tech. degree in computer engineering from the Institution of Engineers (India), Kolkata, India, in 2001, M.Tech. degree in electronics and communication engineering from the Birla Institute of Technology (BIT) (Deemed University), Mesra, Ranchi, India, in 2006 and Ph.D. degree from the Electronics Engineering Department of Aligarh Muslim University, Aligarh, India, in 2013.

He has served for Indian Air Force for 21 ½ years. Until November 2006, he was with Indian Air Force. Since November 2006, he has been in the Dept. of Electronics and Communication Engineering, BIT, where he is currently an Assistant Professor. His Research interests include VLSI/CAD design for classical CMOS, non-classical CMOS and non-CMOS (non-Silicon) technologies [like FinFET, CNFET and STT-based MTJ], SET, Multivalued Logic, Reversible Logic, SRAM, STT-MTJ based MRAM, PCRAM, magnetic logic circuits, robust design of semiconductor memory, power- and variability-aware design, RF nanoscale device modeling, design of ultralow-power nanoscale circuits for portable/wearable/energy-harvesting applications. He is the author or coauthor of more than 159 research papers in reputed journals and conferences. He has also contributed one chapter in 10 books of Springer.

Dr. Islam was a recipient of the National Scholarship (Govt. of India) for three years during study in high school in 1983. He received the best paper award five times in International conferences. He has supervised 35 UG students, 11 PG students and currently supervising 05 PhD scholars. He is a member of Institution of Engineers (MIE), India and senior member of IEEE, USA. He is also currently member of various international bodies / societies such as IEEE Electron Device Society, IEEE Council on Electronic Design Automation, IEEE Sensors Council, IEEE Nanotechnology Council, etc. He is a member of board of editors of various journals. He is reviewer of various international journals such as IEEE Transactions on Circuits and Systems I: Regular Papers, IEEE Transactions on Device and Materials Reliability, IEEE Transactions on Magnetics, Nanotechnology (of IOP publishing), IET Circuits, Devices & Systems, Electronics Letters (of IET), Microelectronics Reliability (of Elsevier), Microelectronics Journal (of Elsevier), Integration, the VLSI Journal (of Elsevier), International Journal of Electronics (of Taylor & Francis), International Journal of Electronics Letter (of Taylor & Francis), International Journal of Circuit Theory and Applications (of Wiley), Circuits, Systems & Signal Processing (of Springer), Frontiers of Information Technology & Electronic Engineering (of Springer), International Journal of Instrumentation Technology (of Inderscience Publishers), International Journal of Nano and Biomaterials (of Inderscience Publishers), etc. He has delivered invited talk/tutorial speech/expert talk in various International Conference /technical programs. He has organized an IEEE international conference and several workshops.