



Dr. Manoj Saxena is currently Associate Professor in Department of Electronics, Deen Dayal Upadhyaya College, University of Delhi, New Delhi, India. He received B.Sc. (with honors), M. Sc., and Ph.D. degrees from the University of Delhi, New Delhi, in 1998, 2000, and 2006 respectively, all in Electronics. During his 15 years of teaching-cum-research career, he has authored or coauthored 210 technical papers in international journals and various international and national conferences. He has contributed one chapter entitled “*MOSFET Modeling*” in the *Encyclopedia on RF and Microwave Engineering* (New York: Wiley, 2005). His current research interests are in the areas of analytical modeling, design, and simulation of Optically controlled MESFET/MOSFET, silicon-on-nothing, insulated-shallow-extension, grooved/concave-gate MOSFETs, cylindrical gate MOSFET and Tunnel FET. He is currently registered as an expert Member in the directory of researchers working in India in the area of Nano Science and Technology, Nano Mission, Department of Science and Technology, Govt. of India. Dr. Saxena has reviewed extensively for IEEE Transactions on Electron Devices, IEEE Electron Device Letters, Semiconductor Science Technology, Solid State Electronics, Journal of Physics: D Applied Physics, Superlattices and Microstructures, International Journal of Numerical Modeling: Electronic Networks, Devices and Fields, MAPAN-Journal of Metrology Society of India and International Journal of Science and Technology Education Research. His name appeared several times in the Golden List of IEEE Transactions on Electron Devices Reviewers. He received outstanding EDS Volunteer recognition from EDS Chapters in the region in 2012. He is currently Fellow IETE, Vice Chair IEEE EDS SRC-Region 10 and Regional Editor of IEEE EDS Newsletter – Region 10 South Asia.

Membership of Professional Bodies - He is Senior Member-IEEE (USA), Member of – Institute of Physics (UK), Institution of Engineering and Technology (UK), National Academy of Sciences India (NASI), International Association of Engineers (Hong Kong), Life-Member of Semiconductor Society of India (New Delhi) India, Life-Member of Indian Science Congress Association (ISCA), Young Associate of Indian Academy of Sciences, Bangalore (India), Joint Secretary of Society for VLSI and Microelectronics, New Delhi (India) and Secretary of IEEE EDS Delhi Chapter, New Delhi.