

Dr. Ajit Kumar Panda, IEEE Distinguished Lecturer

Dr. Ajit Kumar Panda received his Ph.D. degree in Microelectronics area in 1996 from Sambalpur University and was a Post Doctorate Fellow at University of Michigan before joining National Institute of Science and Technology where he is a Professor now. He is a BOYSCAST Fellow. He is the recipient of ICTP Regular Associate Award for the collaboration work with ICTP Microelectronics Laboratory, Trieste. He has visited countries like USA, Italy, German, France, Singapore, Hong Kong, Malaysia, Nepal, Bangkok on different academic assignments. He has published more than 70 international Journal papers and presented his work in more than 100 international/national conferences in the area of Semiconductor Devices, VLSI circuits and Microwave Engineering. He is the coordinator of NIST TIFAC CORE in “3G/4G Communication Technologies” which has established with a funding of around 10Cr from DST, few industries and NIST. He is a senior member of IEEE, member of VLSI Society of India, and Life member of ISTE and fellow of IETE. He is the faculty advisor of NIST IEEE ED Students Chapter. Seven students have awarded PhD under the guidance of Dr. Panda, one student has submitted the thesis and six students have registered for the same. He is a regular reviewer of more than 10 indexed journals including IEEE, IET, and Elsevier etc. He has organized more than 30 conferences at NIST and other places. He is the co-author of a book named “Physics of Semiconductor Devices” and has successfully completed six projects funded by DST, UGC, MHRD, and MSME. His research interest includes Semiconductor Heterostructure devices, VLSI Circuits, Microwave Monolithic Integrated Circuits (MMICs) and Communication Technologies. His group has recently fabricated/tape out a mixed-signal IC using umc 180nm technology and it is first of its kind in Odisha. His B Tech project students were in top 5 in Cadence Design contest in 2012 and winners in 2014 and 2015 in all India competition (only institute to obtain twice the Cadence Design contest in B Tech category). This is considering being one of the biggest successes of Dr. Panda as a technocrat. He has obtained Odisha Technocrat Award for 2015. Recently he has been appointed as one of the DEAN of BPUT and is a member of the NAAC Assessment Committee. He is a Distinguished Lecturer of IEEE Electron Device Society.