

**Bio:**

Alexander Kloes is Professor at THM University of Applied Sciences in Germany and heads the Device Modeling Research Group at the Competence Center for Nanotechnology and Photonics. In 1996 he received the PhD degree in electrical engineering from Technical University of Darmstadt, Germany. From 1997 to 2002 he gained industry experience as Project Manager for Research and Development at Braun Company in Germany, where he contributed to infrared sensor technology on silicon. From this time, he holds 15 patents. Alexander is IEEE Senior Member. His research interests focus on the compact modeling and simulation of semiconductor devices, particularly for nanoscale MOS devices and organic thin-film transistors for flexible electronics. His contributions to the research community cover emerging devices as Junctionless-FETs, multi-gate FETs, Schottky barrier FETs, Tunnel-FETs, ultra-short channel MOSFETs and submicron organic TFTs. He co-authored more than 170 research papers in these areas.