

### Overall statistics & Final review results

<b>Total No. of Submission</b>	<b>301</b>	
<b>No. of Rejection</b>	<b>157</b>	<b>52.20%</b>
<b>No. of Acceptance</b>	<b>144</b>	<b>47.80%</b>
<b>No of Registered papers</b>	<b>143</b>	<b>47.50%</b>
<b>Paper ID</b>	<b>Title of the Paper</b>	<b>Result of review &amp; current status</b>
1	IOT based Temperature Monitoring System	Reject
2	Hard and Soft Switching Geometries & Operations of the MOSFET Used For the SMPS	Accept
3	Brief Study and Review on The Next Revolutionary Autonomous Vehicle Technology	Reject
4	Suitability of the Static Converters For the Power Factor Correction (PFC) Suitability of the Static Converters For the Power Factor Correction (PFC)	Accept
5	Generation of Tunable Low-noise Millimeter-wave Signal using Optical Frequency Comb through Electrical Mixing at 94 GHz	Accept
6	Review of Authentication Algorithms for Comprehensive Threat Analysis and Physical Layer Attacks analysis in Internet of Things	Reject
7	Digital SVC Controller to Analyze Performance of Electrical Distribution System	Reject
8	A Novel Dynamic State Estimation for Power System States Using Square Root Unscented Kalman Filter.	Reject
9	Swastika Shaped Broadband Dielectric Resonator Antenna for S,C,X & Ku Band	Accept
11	Study of 2D Electron Mobility in AlGa <sub>N</sub> /Ga <sub>N</sub> Quantum well Structure	Accept
12	Oscillating Electron Mobility in Double V-shaped Quantum Well based Field Effect Transistor Structure	Accept
13	Analytical Investigation Of Trenched Multi-layered Gate Silicon On Nothing MOSFET With Graded Work-function	Accept
14	Design and Comparative analysis of a Two-Stage Ultra-Low-Power Subthreshold Operational Amplifier in 180nm, 90nm, and 45nm technology	Accept
15	Impact of Source Side Cavity on Sensitivity of Hetero Channel Double Gate MOSFET Biosensor	Accept
16	Investigation of TF-FinFET based Biosensor for Early Diagnosis of protein carrying diseases	Accept
17	In-silico Investigation of Cyl. Gate all Around (GAA) Tunnel Field Effect Transistor (TFET) Biosensor	reject
18	On-chip Analysis of Etched Drain based Cyl. GAA TFET with Elevated Density Strip	Reject

19	Computing Electromagnetic Bandgap for Parallel Nanorod Structure with Double Negative Refractive Index inside Triangular Lattice	Accept
20	Cost Efficient Modular External Elevator for Residential Building	reject
21	PRANet: A Fast Residual Neural Net based Model with Attention Block for Classification of Lung and Colon Cancer from Histopathology Image.	Reject
22	Designing Of Controller for BLDC Driven Solar Water Pump	Reject
23	Scheme of developing an optical Encoder using switching and frequency conversion properties of semiconductor optical amplifiers	Reject
24	A Brief Review on Internet of Things (IoT)	Reject
25	A Comparative Investigation on Characteristics of Conventional MOSFET and Ferroelectric Thin Film Modified FET.	Accept
26	Design of S-Band Source-Controlled MMIC Voltage Variable Attenuator	reject
27	Machine Learning based Malicious Node Detection in IoT Environment	Reject
28	Comparative Analysis of Analog Performance on Addition of Symmetric Source/Drain Underlap to Double Gate AlGaIn/GaN MOSHEMT	Accept
29	Ta2O5 as Tunneling Oxide for n-type Passivated c-Si CS-TOPConSolar Cell	Accept
30	Linearity Performance Analysis of Charge-Plasma-Based Hetero-dielectric Nanotube Tunnel FET	Accept
31	Optical XOR-XNOR logic circuits using mechanical movable mirrors	Accept
32	Analysing Structural Asymmetry on the Nonmonotonic Electron Mobility of Pseudomorphic Heterojunction Field Effect Transistors	Accept
33	Effect of Well Width and Barrier Width on I-V Characteristics of Armchair Graphene Nanoribbon based Resonant Tunneling Diode Structure	Accept
34	Effect of biaxial strain on the electronic structure of Nb-doped WSe2 monolayer: a theoretical study	Accept
35	Numerical Analysis Of An Ultra-High Negative Dispersion Compensating Micro-Structured Optical Fiber With Air-holes Arranged In Octagonal Structure	Accept
36	All Optical Frequency Encoded Dibit-based Inhibitor logic using Reflective Semiconductor Optical Amplifier with Simulative Verification	Reject
37	Exhibiting Dispersive Characteristics at 1330 nm for Optical Pulse Propagation through 2D Kerr Nonlinear Medium	Accept
38	Implementation of Fractional Sample Rate Digital Down Converter for Radio Receiver Applications	Accept
39	Multisubband Electron Mobility in Pseudomorphic Al0.3Ga0.7As/In0.15Ga0.85As Double Quantum Well based FET Structure	Accept
40	Effects of Work Function Variation on the Electrical Performance of sub-7nm GAA FETs	Accept
41	Enhanced DC Performance of Junctionless Field-effect Transistor Using Dielectric Engineering	Accept

42	Design of 18nm AJ DG MOSFET for Bio-Sensing Applications	Accept
43	Dual-Dielectric-Constant Spacer Hetero-Gate-dielectric Junctionless Field-effect Transistor for Improved DC Performances	Accept
45	A Novel Technique of Enhancing CMRR of Fully-Differential Instrumentation Amplifier Using High Gain CMFB Loop	Reject
46	Impact of Channel Engineering on 16nm, 18nm & 20nm Doping-less DG MOSFET	Accept
47	Recessed Channel Carbon Nanotube Truncated Fin Finfet For High Performance ULSI Applicaitons	Accept
48	Novel SPICE Model for bifacial solar cell to increase the renewable power generation	Accept
49	Body Connection Assessment of MOS-Diodes for MOS-Quadrupler based RF Energy Harvesting Circuit.	Accept
50	Injurious Comment Detection and Removal utilizing Neural Network	Reject
51	Capacitive analysis of Strained Superjunction Vertical Single Diffused MOSFET	Accept
52	Detection of Cardiac Arrhythmia based on feature fusion and Machine Learning algorithms	Reject
53	Performance Analysis of FinFET based inverter at 7nm Technology Node Using TCAD Simulation	Accept
54	Computational analysis of doped (10, 0) MoS2 ANR metal junction by Schottky Barrier height modulation	Accept
55	Particular amino acid detection of by Henna leaf extract mediated copper nanoparticles	Reject
56	Statistical method for detection of branch outage including bad data identification	reject
57	Influence of High-k Dielectrics on Nanowire FETs	Accept
58	Nanocomposites based Solid State Electrochromic Nano-Material Membrane Device	Accept
59	Autonomous Remote-monitoring Low-cost Agricultural System	Accept
60	Micro-ECG-Monitoring System Based on Arduino with Bluetooth Feature	reject
61	Coloured Image Processing using GMSK	Reject
62	Power Supply Noise Aware Physical Design with Decoupling Capacitance Allocation in System-on-Chip	Accept
63	A Low Voltage Rectifier for Piezo-Electric Energy Harvesting Designed in CMOS Technology	Accept
64	Design of Cost-efficient Encoder-Decoder using Layered T Logic: A Quantum-dot Cellular Automata Based Approach	REject
65	A Design of Frequency Encoded Dibit-based Comparator Using Reflective Semiconductor Optical Amplifier with Simulative Verification	Accept
67	Design of Bio-inspired cantilever Micro gripper needle for Surgical applications	Reject
68	Power Optimized 10 Bit ADC IP Design in Circuit Level	Accept
69	A non-iterative hybrid Dynamic State Estimation scheme utilizing PMU and SCADA measurements.	Accept
70	MODELING AND SIMULATION OF GaAs NANOWIRE TRANSISTORS	Accept

71	Influence of High-k Oxide Thickness on Gate Stack DMG Junctionless SOI MOSFET	Accept
72	Temperature associated reliability analysis of a Si/Ge Heterojunction Dopingless Tunnel FET considering Interface Trap Charges	Accept
73	Novel 9:1 Ternary Multiplexer on 32nm CMOS Technology	Accept
74	A Practical Multiplayer Multi-armed bandit algorithm for Smart City Communication System	Reject
75	DC Exploration of Oxide Trap Charge Effects on Electrically Doped Nano Ribbon FET	Accept
76	Robust Design of Noise Tolerant 2-Phase Non Overlapping Clock Generating Circuit	Accept
77	AlGa <sub>N</sub> /Ga <sub>N</sub> HEMT with Recessed T-Gate and Floating Metal for High Power Applications	Accept
78	COVID-19 Pandemic Comparative Study of Machine Learning Algorithms	REject
79	An ab-initio study of 2 dimensional metal (Cu, Ag) - 1T' ReS <sub>2</sub> van der Waals heterostructure	Accept
80	TCAD Analysis and Simulation of Double Metal Negative Capacitance FET (DM NCFET)	Accept
81	Impact of Ferroelectric Oxide Layer on Palladium Silicide Source Electrode based Double-Gate Junctionless TFET	Accept
82	Nanowire texturisation of Multi Crystalline Silicon using Silver Assisted Chemical Etching	Accept
83	Auto-tuned optimal PI controllers for MIMO process	reject
84	Magnesium Silicide Source Double Palladium Metal Gate TFET for Highly Sensitive Hydrogen Gas Sensor	Accept
85	Affinity For Vicinity: Transformer Based Cephalometric Landmark Detection	reject
86	Interpretation of Premature Ventricular Abnormality based on Heart Rate Variability Analysis	reject
87	Design and Analysis of High-KGate DielectricEngineered Double Gate MOSFET	Reject
88	Analysis of Static Noise Margin of 10T SRAM Using Sleepy Stack Transistor Approach	Accept
89	Performance analysis of FPGA based MAC units of non-binary number systems for signal processing algorithms	reject
90	Band Offset Dependent Performance Analysis of SnO/Si Heterojunction Solar Cell	Accept
91	Temperature Variation Analysis of SiGe Source based Heterojunction Tunnel FETs	Accept
92	Validation of Input/Output characteristics of Symmetrical Double Source TFET device	Accept
93	Trapezoidal Channel Double Gate Tunnel FET Suitable for better Scalability, Speed and Low Power Application	Accept
94	A Novel Approach to Junction Temperature Management of White PC LED in High Ambient Temperature Industrial Environment	Accept
95	Work-Function modulated GAA MOSFET for Improved Electrostatic Controllability in Lower Technology Node	Accept
96	Study of Different Dielectrics For Surface Passivation of Ga <sub>N</sub> HEMT	Reject
97	HeartHealth: An Intelligent Model for Multi-Attribute Based Heart Condition Monitoring using Fuzzy-TOPSIS Method	Accept

98	Impact of Mole Fraction Variation on the Performance of GaAs <sub>1-x</sub> Sb <sub>x</sub> FinFET	Accept
99	Ambipolarity suppression in SiGe/Si-TFET using hetero-dielectric BOX engineering	reject
100	SIC-TPG for path delay fault detection in BIST Application	Accept
101	A 3.86 $\mu$ W Miller-Compensated Inverter Transimpedance Amplifier for Photoplethysmography Sensing	reject
102	Simulation and Performance Analysis of novel InN-GaN-BTG-MOSFET	Accept
103	Lower Fin Modulation Analysis for a Novel 5nm Top Bottom Gated Junctionless FinFET for Improved Performance	Accept
105	Asymmetric Gate Stack Triple Metal Gate All Around MOSFET (AGSTMGAAFET) for Improved Analog Applications	Accept
106	A Comprehensive Survey on Communication Technologies for a Grid Connected Microgrid System	reject
107	Multiband Resonance in Symmetry Broken Planar Terahertz Metamaterial	Accept
108	Analog Performance of Dual- Material Double Gate Junctionless Accumulation-Mode Cylindrical Gate All Around (DMDG-JLAM-CGAA) MOSFET with High-k Gate Stack	Accept
109	Design of a Modified 8-bit Semiflash Analog to Digital Converter	Accept
110	PV BASED POWER GENERATION AND POWER QUALITY IMPROVEMENT USING SSSC POWER COMPENSATION	Reject
111	CLUSTER BASED COMMUNICATION IN WSN: A SURVEY	reject
112	Self-directed Robot for Car Driving using Genetic Algorithm	reject
113	Impact of Channel Splitting on Gate All Around Tunnel Field Effect Transistor (GAATFET)	Accept
114	Learning Based Channel Selection in the Cognitive Radio Network	reject
115	Simple and easy one step method for synthesizing ZnO nanorods for high frequency device application	Accept
116	An Efficient Way to Analyze Hysteresis Loss of an Inverter-fed Transformer	reject
118	ML and DL algorithms for Diabetes Classification	reject
119	Synchronous Duty Cycle in MAC protocol for WSN Applications	reject
120	Analysis of Diabetes Patients using Classification algorithms	reject
121	AI Algorithm System for Prediction of Diabetes Using Progressive Web App and Cloud	reject
122	Solar Powered Home Automation System Enhanced with Internet of Things and Network Time Protocol Server	Accept
123	Novel IoT Based Smart Blind Guidance System To Maintain Social Distancing	Reject
124	Symmetrical and Asymmetrical Fault Studies of Wind-Farm Embedded Power Network	Accept
125	Complementary Memristive Switch Based Realization of Delay and Toggle Flip-Flop	Accept
126	Timing Optimization of Functional Unit Block in High Speed Core	reject
127	IoT based remote patient health monitoring system	reject

128	Serial and Parallel based Intrusion Detection System using Machine Learning	Accept
129	L-shaped Piezoelectric energy harvester for low frequency application	Accept
130	Study on Power Quality Phenomena of Grid connected AC Micro-grid	Accept
131	Strained SiGe Channel TFTs For Flexible Electronics Applications	Accept
132	Segmentation and Classification of cerebral vascular attack region in the brain MRI images using Artificial Neural Network	reject
133	A 1.2-V 100.74-dB SNDR $\Sigma\Delta$ M using Self-biased Inverter-based Amplifiers for Wearable Devices	reject
134	Parametric Analysis for Varied Gate Work function in Trigate n-channel FinFET	Accept
135	Linearity Investigation of Ultra-Low-Power Cylindrical SOI Schottky Barrier MOSFET for Biomedical and 5G/LTE Circuits Application	Accept
136	Generalized Memristor Model using Simulink and its Rectification for Sinusoidal and other Periodic Signals	Accept
137	Gate - Stack Dual Metal (DM) Nanowire FET with Enhanced Analog Performance for High Frequency Applications	Accept
138	An Area and Power Efficient VLSI Architecture for 1-D and 2-D Discrete Wavelet Transform (DWT) and Inverse Discrete Wavelet Transform (IDWT)	Accept
139	Low Noise Current Integration Inverter Cascode Transimpedance Amplifier for Wearable Photoplethysmography Sensing	reject
140	Secure Payment System in Blockchain	reject
141	Efficient rectifier for piezoelectric energy harvester using active diode	Accept
142	An assessment of different CMOS technologies	reject
143	FPGA based Implementation of Approximate Multiplier Architecture	Reject
144	Whisper to Neutral Speech Conversion	reject
145	All-Optical Frequency Encoded Dibit-based Half Adder using Reflective Semiconductor Optical Amplifier with Simulative Verification	Accept
146	Numerical Simulation of GaN-BTG MOSFET for Suppression of SCEs	Accept
147	BOFSC:A Blockchain Based Decentralized Framework to Ensure the Transparency of Organic Food Supply Chain	reject
148	An Innovative High-Frequency Wireless Communication Technique Inspired by Multi-Frequency Carrier Signals	reject
149	Features of Snapback in Compact Memory Devices For High Performance Integrated Circuits	Accept
150	Minimizing Wirelength with Bend Reduction using Gradient Descent PSO Hybrid in VLSI Global Routing	Accept
151	Determination of Optimum Dynamic Threshold for Visual Object Tracker	reject
152	Technology Computer Aided Design of a Novel Fully Gate Covered Channel Junctionless SOI FinFET for high performance analog applications	Accept

153	Study on Voltage Balancing Problem of Three phase Three-level Neutral-point Diode-clamped Inverter for Both Balanced and Unbalanced Loads using MATLAB Simulink Software	reject
154	Development of Smart Multi-level Inverter with Remote Monitoring System	reject
155	A Simulation Study of 2-D Electron Gas in GaN HEMT for High-Speed Applications	Accept
156	Investigation of III-V Tunnel FETs for Analog Circuit Design	Accept
157	Analysis of Dual Metal Gate Engineered SiGe/Si TFET based Biosensor: A Dielectric Modulation Approach	Accept
158	FPGA Implementation of an Image Watermarking Scheme based on Intensity Level Matching	Accept
159	In memory Computing based Boolean and logical Circuit Design using 8T SRAM	Accept
160	Impact of Au and Ag contact on the electrical parameters of MnO <sub>2</sub> Nanowires grown by GLAD technique	Accept
161	Recognition and Classification of Accompanying Audios of Kathak Dance	reject
162	Study of various Adder circuits and comparison of their power Efficiencies with a innovative XNOR logic based adder.	reject
163	Design and Analysis of III-V Tunnel FET based Energy Efficient Digital Circuits	Accept
164	Design and Performance Analysis of Z-Shaped Charge Plasma TFET based label-free Biosensor	Accept
165	NEURAL NETWORK BASED SOFT SENSOR FOR CRITICAL PARAMETER ESTIMATION OF GAS TURBINE ENGINE	Accept
166	Simulation of Low Power DVCC based LNA for Wireless Receiver	Accept
167	Surface Potential based Analytical Modeling of a Strained Macaroni Channel MOSFET	reject
168	Exploration of 9T SRAM Cell for In Memory Computing Application	Accept
169	Investigation of linearity issues in nonlinear analog model of Artificial Neuron using TSMC 180nm Technology	reject
170	All-optical n-bit Binary to Two's Complement Converter with the help of semiconductor Optical Amplifier-assisted Sagnac Switch	Reject
171	Hand Sign Recognition System	reject
172	Fin aspect ratio optimization of Junctionless Accumulation Mode (JAM) Gate Stack Gate All Around (GS-GAA) FinFET for improved wireless performance	reject
173	The Performance Analysis of 70nm T-gate InAlN/AlN MOS-HEMT using Graded Buffer	Accept
174	Smart Street Light Management System with Automatic Brightness Adjustment Using Bolt IoT Platform	Reject
175	Stress-Engineered AlGaIn/GaN High Electron Mobility Transistors Design	Accept
176	Harmonic Suppression in a Folded Hairpin-Line Cross-Coupled Bandpass Filter by using Spur-Line	Accept
177	A Novel Junction Less Dual Gate Tunnel FET with SiGe Pocket for Low Power Applications	Accept
178	A Dielectric Modulated MOS transistor for Biosensing	Accept



179	Determination of critical contingency and impact assessment on power system	reject
180	IoT enabled Soil Sensing Device and Its Application for Maximizing Productivity	reject
181	A Review on Machine Learning Approaches for Predicting the Effect of Device Parameters on Performance of Nanoscale MOSFETs	Accept
182	Study of Lead-Free Perovskite and Quantum Dots Core-Shell Infrared Photodetector Integrated with the Silicon Technology	Accept
183	A 0.6 V 1.6nA Constant Current Reference with Improved Power Supply Sensitivity	Accept
184	Challenges Facing Middleware and Middleware Platform for Internet of Things-A Review	reject
185	Variation of Rain Rate Effects for Terrestrial Communication at Frequencies above 10 GHz	Accept
186	A 2.24 NEF Current-Balancing Instrumentation Amplifier Using Inverter-Based Transimpedance Stage for ECG Signal Acquisition in 180nm Technology	Accept
187	A Comparative Study of Temperature Dependency on Electrical Parameters of FinFET for Al <sub>2</sub> O <sub>3</sub> and HfO <sub>2</sub> Dielectric Materials	reject
188	Low Power Radiation-Hardened 12T-SRAM Bit Cell with Faster Performance for Space Applications	Accept
189	Machine Learning Algorithm for Cardiovascular Disease Prediction	reject
190	A STUDY ON CORONAVIRUS PANDEMIC ANALYSIS USING DEEP LEARNING TECHNIQUES	reject
191	TransProc: A Novel Reconfigurable Architecture for Linear Transformations applied to Image and Signal Processing	reject
192	Effect of Drain Engineering on DC and RF Characteristics in Ge-source SD-ZHP-TFET	Accept
193	COVID-19 DIAGNOSING WITH CNN MODEL VGG-19 OVER CHEST X-RAY IMAGES	reject
194	DESIGN STUDIES OF DIFFERENT CMOS THRESHOLD LOGIC BASED FULL ADDER CIRCUITS USING TSPICE	Accept
195	Double Gate-Pocket-Junction-less Tunnel Field Effect Transistor	Accept
196	Design of Modified PFAL for Ultra Low Power High Performance Computing & Systems	reject
197	I-Fresh: An IoT Based System for Predicting the Freshness Of Vegetables and Flower	reject
198	AN INTELLIGENT WEATHER PREDICTION SYSTEM BASED ON IOT	Accept
199	GaN-Based Two-Stage Colpitts Oscillator for Wireless Power Transfer	Accept
200	Design of Parameter Estimators for First-order Control Model	reject
201	CNN Architecture for Diabetes Classification	reject
202	Authencation and secure communication by Haar Cascade Classifier , Eigen Face , LBP Histogram and variable irreducible polynomial in (28 ) finite field	Accept
203	NEURAL NETWORK BASED INTEGRATED MODEL FOR INFORMATION RETRIEVAL	reject



204	TRAFFIC ACCIDENT PREDICTION BASED ON CNN MODEL	reject
205	Impact of temperature and different types of trap charges on noise behavior of Non-uniform Body with Dual Material Source TFET (NUTFET-DMS)	Accept
206	Analysis of Junctionless Double-Gate MOSFET for Energy Efficient Digital Application	Accept
207	Analysis of three and four stage OTA buffer amplifier for driving resistive load < 100 Ohm	Accept
208	Design of Duty Ratio and Phase-Shift Control Circuits for ZV-ZCS PSFB Converter Tracking MPP of SPV Source	Accept
209	Power Dissipation Estimation in SWCNT based Interconnects	Accept
210	Highly Linearized GaN HEMT Based Class E/F3 Power Amplifier	Accept
211	Design and Simulation of Clock and Data Recovery Circuit (CDR) in 180nm CMOS Technology	Reject
212	Design of Radiation Hardened 1-bit SRAM using 180nm technology	reject
213	A 1.8V 5-bit Segmented Current Steering Digital-to-Analog Converter	Accept
214	Design and Analysis of Ring Multiband Metamaterial Absorber	reject
215	Disease Prediction using Various Data Mining Techniques	reject
216	Superior NBTI in Si/SiGe/Si Gate-All-Around MOSFET	reject
217	Implementation of Automatic Pet feeder using myRIO	reject
218	Speech Emotion Recognition System With Librosa	reject
219	Design and Implementation of Low power FinFET based Compressor	reject
220	Design of Sub-20nm FinFET based label free biosensor	reject
221	Design and Implementation of Smart Health Care Monitoring System Using FPGA	reject
222	A Data Security module based on Crypto and Steganography techniques	reject
223	Design of a Sensor System using Fiber Bragg Grating for Liquid Level and Liquid Density Measurement	reject
224	Maintaining the Expected Outcome of a Control System with the Introduction of Additional Actuating Signal (AAS)	reject
225	A Framework for Post Disaster Management using Device to Device Communication with Controlled Mobility and Opportunistic Routing	Accept
226	Electromigration Immune Copper/CNT based Hybrid Power Distribution Network	reject
227	A Study on Herd Behavior Using Sentiment Analysis in Online Social Network	reject
228	The impact of oxide layer width variation on the performance parameters of FinFET	Accept

229	Highly Efficient and Compact Silicon based Novel Michelson Interferometer Modulator	Accept
230	Performance Analysis of Single Operational Transresistance Amplifier Based Bandpass Filter and Bandreject Filter	Accept
231	Impact of Ge Grading Profile on the Performance Characteristics of SiGe Heterojunction Bipolar Transistors	Accept
232	Deep Learning Method for Brain Tumor Identification with Multimodal 3D-MRI	reject
233	Study of Silicon-Based Tubular Tunnel FET for Biosensing Application	Accept
234	Study of SiGe-Si Source stacked in Silicon Nano tube Tunnel FET	Accept
235	Heterogeneous wireless sensor networks Clustering and Energy Efficient Routing Optimization Algorithm	reject
236	Fault Detection and Isolation in Industrial Processes Based On Rnn : Deep Learning Approach	reject
237	An Online Testing Technique for the Detection of Control Nodes Displacement Faults (CNDF) in Reversible Circuits	reject
238	Metaheuristic algorithms-based approach for optimal design of improvised fully differential amplifier for biomedical applications	Accept
239	Study of Threshold Voltage Variation of Work function Engineered Gate Recessed IR SOI MOSFET	reject
240	Scientific validation of Biofield (Aura) for use as Complementary and Alternative Medicine (CAM)	reject
241	Energy Efficient Decoder Circuit Using Source Biasing Technique in CNTFET Technology	Accept
242	Nutrient Food Prediction through Deep Learning	reject
243	Comparative study on Analog & RF Parameter of InAlN/AlN/GaN Normally off HEMTs with and without AlGaIn Back Barrier	Accept
244	Study of various Adder circuits and comparison of their power Efficiencies with a innovative XNOR logic based adder.	reject
245	TUNNEL-FET BASED BIOSENSOR USING HETERO-JUNCTION TECHNIQUE WITH DIELECTRIC POCKETING	reject
246	Impact of SiO <sub>2</sub> Isolation in Ge/Si Tunnel Field-Effect Transistors	reject
247	AUTOMATED SMART ATTENDANCE SYSTEM USING FACE RECOGNITION	reject
248	An Energy Efficient Secured Arduino based Home Automation using Android Interface	reject
249	Design and performance analysis of 20nm Si based DG MOSFET	reject

250	Degradation of MOSFET pn junction reverse-bias leakage current by structure engineering	reject
251	Real-Time Vehicle Counting with Traffic Flow Control System	reject
252	Real-time Sign Language Recognition using Computer Vision	reject
253	Real-time Monitoring of Water Quality in Fisheries	reject
254	Performance Analysis of Diagonal-Transposition Based Multilevel Keyless Speech Cryptosystem	reject
255	Email Spam Filtering using RNN and LSTM	reject
256	Performance Analysis of Heterojunction Tunnel FET Biosensor	reject
257	MMSE-Spectral Amplitude Based Techniques For Single Channel Speech Enhancement Objective Measures	reject
258	An SOI n-p-n Double Gate TFET for Low Power Applications	Accept
259	Design Approach for Parameter Estimators for One-parameter First-order Scalar Plant	Accept
260	Ultra Low-Power Low-Pass Filter Design for Wearable Biomedical Applications	Accept
261	Optimization of Mixed Sn and Pb Perovskite Solar Cell in Terms of Transport Layers and Absorber Layer Thickness Variation	Accept
262	The Role of Zinc in Covid 19 Pathogenesis	reject
263	CMOS based Low Power Full swing XOR design on 45nm technology	reject
264	ANALYSIS OF CMOS BASED ADIABATIC FLIP-FLOPS FOR ULTRA LOW POWER APPLICATIONS	reject
265	Design and Analysis of Ultra low power CMOS based Operational Trans-conductance Amplifier (OTA) for Biomedical Instruments	reject
266	A Low Voltage NMOS current bleeding down conversion Mixer with source degeneration in 0.18 $\mu$ m CMOS technology	Accept
267	Comparative Study of Electrical Equivalent Models of Solar Photovoltaic cell	reject
268	DFT Based approach to sense SF6 decomposed gases (SO <sub>2</sub> , SOF <sub>2</sub> , SO <sub>2</sub> F <sub>2</sub> ) using Ni doped WS <sub>2</sub> monolayer	Accept
269	Synchronous Duty Cycle in MAC protocol for Wireless Applications	reject
270	Analytical Model For Tunnel Current Through 1D Channel in Advanced nano MOSFET	Accept
271	Analysis of electrical properties of unpolarized/polarized CNT-BNNT-CNT for varying lengths of BNNT	Accept

272	First Principle Study of MoS2 adsorbed Transition Metal for Sensing Urea and Methanol	Accept
273	First Principle Study of MoS2 adsorbed Transition Metal for Sensing NH3 and CH4	Accept
274	Application Of Biosensor For Assessing Food Quality	reject
275	Applications of Biosensors for Dairy Industry	reject
276	Study of VLSI Architecture for Serial Multiplier	reject
277	Design of Walsh-Hadamard Code Using Optical Nonlinear Material	reject
278	A investigative Study of Cancer Detection Using Various Type of Qdots	reject
279	performance invetsigation of Recessed Gate Mg-doped P-type In0.2Ga0.8N cap AlGaIn/GaN/AlGaIn DH-HEMT	reject
280	Energy harvesting application of wind induced vibration	reject
281	Modelling of Sequential Adiabatic Logic for Ultra Low Power Applications	reject
282	MASH 1-1-1 for Fractional-N Frequency synthesizer in Zynq-7000 series SoC Platform	reject
283	Design and Numerical Analysis of Tri-band Terahertz Metamaterial	reject
284	Analysis of Polymeric Microchannel employing Pulsed Laser Ablation	reject
285	Study of gate tunneling in ultrathin low-power MOS devices with multi- oxide stack	reject
286	Study of Automation in railway platform & no man railway gate using IOT	reject
287	Performance Invetsigation of 2PASCL,2PADCL & ADCL energy recovery logics	reject
288	Two-Stage CMOS Op-amp design suing alpha-power model	reject
289	Theoretical Study of Thermoelectric Transport Parameters in AlGaIn/GaN Systems	reject
290	Analysis of Power & Delay in a Adiabatic Vedic Multiplier	reject
291	Analaysis of scattering in an Asymmetric Coupled Double Quantum Well Structure	reject
292	Terahertz Characterization of the prepared cellulose from microbial source	reject
293	Hardware Architecture of a Decoder for Fractal Image Compression	reject
294	Linearity performance study of a downscaled JL DG MOSFET	reject
295	RF performance study of a downscaled JL DG MOSFET	reject

296	Performance investigation of FinFET for Low-Power applications	reject
297	Modelling of Surface Potential and Threshold Voltage for JLDG MOSFET	reject
298	Performance analysis of Strain on the Performance of JLTFT	reject
299	Design Optimization Techniques for the Low Power CMOS VLSI	reject
300	Green Charging Station for Electrical Vehicle (EV)	reject
301	A review of MOS transistor Scaling	reject