

## Short Biography of Prof. (Dr.) Mridula Gupta:

Professor Mridula Gupta completed her Bachelor, Master's, M. Tech and Ph. D degree from University of Delhi. She joined Department of Electronic Science, University of Delhi as Lecturer in 1989 and was promoted to Professor in 2006 and currently she is Head of the Department (HOD). She is also Joint Dean Student Welfare, University of Delhi. She received the University Gold Medal for being the best candidate in the M.Sc. and Stood first in the order of merit in M.Tech. She was also awarded the Nehru Centenary Common Wealth Fellowship (UK) in 1990. As chairperson of IEEE EDS Delhi Chapter, she received IEEE EDS best Chapter of the year award in 2013 (On basis of work done in 2012) and again in 2017 (On basis of work done in 2016). She is currently Chairperson of IEEE-EDS Delhi Chapter since 2019. Her name is also listed in Top 50 Research Guides across India in 2019 by Ulektz Wall of Fame.

Her field of research is Modeling & characterization of submicron and deep submicron field effect devices. She has published more than 220 papers in various International Journals, 320+ papers in International and National Conference proceedings, supervised 27 Ph. D students & 6 are working more. She is also Member of Board of Directors of Tech. Next Lab. Pvt. Ltd. She is Senior IEEE Member, Fellow of the IETE, Life Member of Semiconductor Society of India and Life Member & Secretary of Society for Microelectronics and VLSI, India. She has written book chapter on "MOSFET Modeling" in *Encyclopedia of RF & Microwave Engineering*, John Wiley U.S.A, "Current collapse in AlGaIn/GaN HEMTS" in *Handbook for III-V High Electron Mobility Transistor Technologies*, CRC Press, Taylor & Francis Group & has edited conference proceedings of six national and international conferences. She has organized more than 25 International and National Conference/Workshops/Seminars etc in different administrative positions and has delivered more than 15 invited/keynote talks within India and abroad. She has handled 24 Research projects worth 3.6 Crores funded by UGC, CSIR, DST, DRDO and DU. She has attended Leadership for Academicians Programme (LEAP) organized by University of Delhi in collaboration with University of Cambridge, United Kingdom in 2020. She is also Co-Project Investigator of Samarth Project - Complete eGov Suite for HEIs - Smarter Automation Engine for Universities Initiative under National Mission in Education through ICT.

