

Short Biography of Dr. Soumya Pandit:

Dr. Soumya Pandit is currently an Assistant Professor, Stage-III in the [Institute of Radio Physics and Electronics](#), University of Calcutta, India. He received the B.Sc (Physics Honours), M.Sc (Electronic Science) and M.Tech (Radio Physics and Electronics) degrees from the [University of Calcutta](#) in 1998, 2000 and 2002, respectively. Shri. Pandit started his career as Lecturer at Meghnad Saha Institute of Technology, under Maulana Abul Kalam University of Technology. After graduating with a Ph.D. degree in the domain of VLSI Design from the Indian Institute of Technology, Kharagpur, he joined the department of Radio Physics and Electronics, University of Calcutta as an Assistant Professor in 2008. He had researched in developing design methodologies and associated computer-aided design tools for High-level Synthesis of CMOS Analog Circuits in his Ph.D. work. He served as Research Consultant at Advanced VLSI Design Laboratory, IIT Kharagpur from 2003 to 2008. During this period he acted as lead project scientist in several R&D projects sponsored by semiconductor industries like National Semiconductor, USA and government agencies like DeitY.

Dr. Pandit has successfully taped out several integrated circuits in 0.8 micron and 0.18-micron CMOS technology. Dr. Pandit has published more than 50 number of papers in leading international journals and conferences. He has authored a book on Nano-Scale CMOS Analog Circuits: Models and CAD Techniques for High Level Design, published by CRC Press, USA and several book chapters in edited volumes published by CRC Press, Springer, IET. Dr. Pandit had successfully completed 4 R&D projects sponsored by DST, Govt. of India, TEQIP, Phase-II, University of Calcutta and others. He was the Chief Investigator of Special Manpower Development Program-Chip to System Design (SMDP-C2SD) Project at the University of Calcutta. Dr. Pandit has developed IC Design Laboratory at the Department of Radio Physics and Electronics, University of Calcutta, meant for post graduate teaching and research students. His current research interest includes CMOS Analog Circuit Design using SPICE, statistical analysis of electronic circuits, compact modeling of advanced semiconductor devices. He has profound interest on developing learning materials suitable for online education.

Dr. Pandit is a Senior Member of IEEE, USA. He is currently the Vice Chair, SRC, Region-10, IEEE Electron Devices Society, USA. He is the founder Chapter Adviser of the IEEE Electron Devices Society, University of Calcutta Student Chapter. He served as the Chair of IEEE EDS Kolkata Chapter during 2014-2015. Dr. Pandit is a Chartered Engineer [India] in Electronics and Telecommunication Engineering.



