IEEE Student Branch at LIU-Brooklyn & Cybernetics Study Association (CSA)
October 12, Friday, 6~ 8:00 PM
Center for Entrepreneurship & Innovation
Pratt Building Room 110

Distinguished Lecture in CyberSecurity

Prof. Ramesh Karri, Co-Chair, Center for CyberSecurity, NYU, Professor of Electrical and Computer Engineering, NYU-Tandon.

Distinguished Lecturer: Prof. Ramesh Karri is a **Professor of Electrical and Computer Engineering at Tandon School of Engineering, New York University.** He has a Ph.D. in Computer Science and Engineering, from the University of California at San Diego. His research and education activities span hardware cybersecurity including trustworthy ICs, processors and cyberphysical systems; security-aware computer aided design, test, verification, validation and reliability; nano meets security; metrics; benchmarks; hardware cybersecurity competitions; additive manufacturing security.

He has over 200 journal and conference publications including tutorials on Trustworthy Hardware in IEEE Computer (2) and Proceedings of the IEEE (5). His groups work on hardware cybersecurity was nominated for best paper awards (ICCD 2015 and DFTS 2015) and received awards at conferences (ITC 2014, CCS 2013, DFTS 2013 and VLSI Design 2012) and at competitions (ACM Student Research Competition at DAC 2012, ICCAD 2013, DAC 2014, ACM Grand Finals 2013, Kaspersky Challenge and Embedded Security Challenge).
He was the recipient of the Humboldt Fellowship and the National Science Foundation CAREER Award. He is the area director for cyber security of the NY State Center for Advanced Telecommunications Technologies at NYU-Poly; Co-founded the NYU Center for CyberSecurity -CCS (http://cyber.nyu.edu/), co-founded the Trust-Hub (http://trust-

hub.org/) and founded and organizes the Embedded Security Challenge, the annual red team blue team event at NYU, (http://www.nyu.edu/csaw2016/csaw-embedded).

He co-founded the IEEE/ACM Symposium on Nanoscale Architectures (NANOARCH). He served as program/general chair of conferences including IEEE International Conference on Computer Design (ICCD), IEEE Symposium on Hardware Oriented Security and Trust (HOST), IEEE Symposium on Defect and Fault Tolerant Nano VLSI Systems (DFTS) NANOARCH, RFIDSEC 2015 and WISEC 2015. He serves on several program committees (DAC, ICCAD, HOST, ITC, VTS, ETS, ICCD, DTIS, WIFS).

He was the Associate Editor of several IEEE Transactions such as IEEE Transactions on Information Forensics and Security (2010-2014), IEEE Transactions on CAD (2014-present), ACM Journal of Emerging Computing Technologies (2007-present), ACM Transactions on Design Automation of Electronic Systems (2014-present), IEEE Access (2015-present), IEEE Transactions on Emerging Technologies in Computing (2015-present), IEEE Design and Test (2015-present) and IEEE Embedded Systems Letters (2016-present). **He served as an IEEE Computer Society Distinguished Visitor (2013-2015).** He is on the Executive Committee of IEEE/ACM Design Automation Conference initiating and leading the Security@DAC initiative (2014-2017). He has delivered invited keynotes, talks, and tutorials on Hardware Security and Trust (ESRF, DAC, DATE, VTS, ITC, ICCD, NATW, LATW, CROSSING etc). **His Research Interests are Trustworthy Hardware, Nanoscale Architectures, Nano-enabled Security and Assurance, Computer Aided Design of Fault-Tolerant VLSI systems.**

Agenda: 6:00 ~ 6:30 PM (Refreshments), 6:30 ~ 6:45 PM (Opening Remarks by Dean Ray Pullaro, School of Business, LIU Brooklyn), 6:45~ 7:45PM (Distinguished Lecture Presentation), 7:45 ~8:00 PM (Q/A). **ALL ARE WELCOME**